

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,202,532 B2
APPLICATION NO. : 10/761144
DATED : April 10, 2007
INVENTOR(S) : Czech et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page, item [54]

Delete the current title and insert --VOLTAGE COUPLING CIRCUIT FOR AN INTEGRATED CIRCUIT--

Column 1

Line 1, delete "INTEGRATED CIRCUIT" and insert --VOLTAGE COUPLING CIRCUIT FOR AN INTEGRATED CIRCUIT--

Signed and Sealed this

Twenty-ninth Day of May, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office